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Franaszek et al.Filing Date:  
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## U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
L	1	4,807,110	02/21/1989	Pomerene et al.	711	213	04/06/1984
L	2	5,361,391	11/01/1994	Westberg	395	425	06/22/1992
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EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
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L	2	Distributed Prefetch-buffer/Cache Design for High Performance Memory Systems, pp 254-263, Departments of Computer Science and Electrical Engineering, Duke University, Durham, NC, 1996 - by Thomas Alexander and Gershon Kedem.
L	3	DRAM-Page Prediction and Prefetching, pp. 267-275, Computer Science Department, Duke University, Durham, NC, 2000 by Haifeng Yu and Gershon Kedem.
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L	5	TCP: Tag Correlating Prefetchers, by Zhigang Hu of IBM Corp.; Margaret Martonosi of Princeton University; and Stefanos Kaxiras of Agere Systems.
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Examiner

Date considered

7/13/06

EXAMINER: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.